

IN THE CLAIMS

Please amend claims 1, 5, 8, 11, 15 and 18 as follows:

1 1. (Previously Presented) A method of designing a video signal processing
2 integrated circuit (IC), comprising the steps of:

3 providing the video signal processing IC with having an envelope detector for
4 detecting and outputting an envelope of a frequency modulated (FM) video signal;

5 wherein:

6 providing the video signal processing IC with a level variation switching circuit
7 for changing an envelope level of the FM video signal ~~according to an execution mode is~~
8 ~~incorporated into the video signal processing IC;~~ and

9 connecting an input of the level variation switching circuit to a control output of a
10 microprocessor so that ON/OFF switching control of the level variation switching circuit
11 is executed in response to a control data input from [[a]] the microprocessor.

1 2. (Original) The method according to claim 1, wherein the level variation
2 switching circuit reduces variation in the envelope level of the FM video signal according
3 to standard playback (SP) mode information and super long playback (SLP) mode
4 information, respectively, contained in the control data input from the microprocessor.

1 3. (Original) The method according to claim 1, wherein the level variation

2 switching circuit operates in dependence on a playback mode of a video cassette recorder.

3 4. (Original) The method according to claim 1, wherein the level variation
4 switching circuit has a resistor at an output terminal of the envelope detector.

5 5. (Previously Presented) A video signal processing integrated circuit (IC)
6 incorporating an envelope detecting circuit for detecting an envelope level of an FM
7 video signal, wherein the envelope detecting circuit comprises:

8 a peak detector for receiving the FM video signal and for detecting a peak value of
9 the FM video signal; and

10 a level switch having a first input connected to an output of the peak detector and
11 having a second input connected to a control output of a microprocessor for controlling
12 the envelope level of the FM video signal according to mode information applied from
13 [[a]] the microprocessor so as to reduce a variation in the envelope level in accordance
14 with a type of mode of operation of a video cassette recorder (VCR).

1 6. (Original) The video signal processing IC according to claim 5, further
2 comprising an amplifier connected to an input terminal of the peak detector for
3 amplifying the FM video signal with a predetermined gain prior to provision to the peak
4 detector.

1 7. (Original) The video signal processing IC according to claim 5, further
2 comprising an amplifier connected to an output terminal of the peak detector for
3 amplifying the FM video signal with a predetermined gain after processing in the peak
4 detector.

1 8. (Previously Presented) [[The]] A video signal processing ~~IC according to~~
2 claim 5, integrated circuit (IC) incorporating an envelope detecting circuit for detecting
3 an envelope level of an FM video signal, wherein the envelope detecting circuit
4 comprises:

5 a peak detector for receiving the FM video signal and for detecting a peak value of
6 the FM video signal; and

7 a level switch connected to an output of the peak detector for controlling the
8 envelope level of the FM video signal according to mode information applied from a
9 microprocessor so as to reduce a variation in the envelope level in accordance with a type
10 of mode of operation of a video cassette recorder (VCR);

11 wherein the level switch includes a resistance element having a first terminal
12 connected to the output of the peak detector and having a second terminal, and a
13 switching control element connected to the second terminal of the resistance element, the
14 switching control element being controlled by the mode information from the
15 microprocessor.

1 9. (Original) The video signal processing IC according to claim 8, wherein the
2 mode information comprises SP/SLP mode information relating to operation of the VCR.

1 10. (Original) The video signal processing IC according to claim 5, wherein the
2 mode information comprises SP/SLP mode information relating to operation of the VCR.

1 11. (Previously Presented) A method of designing a video signal processing
2 integrated circuit (IC) having an envelope detector for detecting an envelope of a
3 frequency modulated (FM) video signal, said method comprising the steps of:

4 providing a level variation switching circuit in the video signal processing IC for
5 changing an envelope level of the FM video signal according to an execution mode;

6 connecting an input of the level variation switching circuit to a control output of a
7 microprocessor; and

8 providing an ON/OFF switching control of the level variation switching circuit in
9 response to a control data input from [[a]] the microprocessor, said control data input
10 containing playback mode information relative to the FM video signal.

1 12. (Original) The method according to claim 11, further comprising the step of
2 providing the level variation switching circuit with a capability of reducing variation in
3 the envelope level of the FM video signal according to standard playback (SP) mode
4 information and super long playback (SLP) mode information, respectively, contained in

5 the control data input from the microprocessor.

1 13. (Original) The method according to claim 11, wherein the level variation
2 switching circuit operates in dependence on a playback mode of a video cassette recorder.

3 14. (Original) The method according to claim 11, wherein the level variation
4 switching circuit has a resistor at an output terminal of the envelope detector.

1 15. (Previously Presented) A video signal processing circuit for detecting an
2 envelope level of an FM video signal input thereto, said circuit comprising:

3 peak detector means for receiving the FM video signal and for detecting a peak
4 value of the FM video signal; and

5 level switch means connected to said peak detector means for controlling the
6 envelope level of the FM video signal according to playback mode information relating to
7 a mode of operation of a video cassette recorder (VCR), said playback mode information
8 being applied thereto to said level switch means so as to reduce a variation in the
9 envelope level in accordance with [[a]] the mode of operation of [[a]] the video cassette
10 recorder (VCR).

1 16. (Original) The video signal processing circuit according to claim 15, further
2 comprising an amplifier connected to an input terminal of said peak detector means for

3 amplifying the FM video signal with a predetermined gain prior to provision to said peak
4 detector means.

1 17. (Original) The video signal processing circuit according to claim 15, further
2 comprising an amplifier connected to an output terminal of said peak detector means for
3 amplifying the FM video signal with a predetermined gain after processing in said peak
4 detector means.

1 18. (Previously Presented) [[The]] A video signal processing circuit according
2 to claim 15, for detecting an envelope level of an FM video signal input thereto, said
3 circuit comprising:

4 peak detector means for receiving the FM video signal and for detecting a peak
5 value of the FM video signal; and

6 level switch means connected to said peak detector means for controlling the
7 envelope level of the FM video signal according to mode information applied thereto so
8 as to reduce a variation in the envelope level in accordance with a mode of operation of a
9 video cassette recorder (VCR);

10 wherein said level switch means includes a resistance element having a first
11 terminal connected to said peak detector means and having a second terminal, said level
12 switch means further including a switching control element connected to the second
13 terminal of the resistance element, the switching control element being controlled by the

14 mode information applied to said level switch means.

1 19. (Original) The video signal processing circuit according to claim 18,
2 wherein the mode information comprises SP/SLP mode information relating to operation
3 of the VCR.

1 20. (Original) The video signal processing circuit according to claim 15,
2 wherein the mode information comprises SP/SLP mode information relating to operation
3 of the VCR.